

DE LA RECHERCHE À L'INDUSTRIE

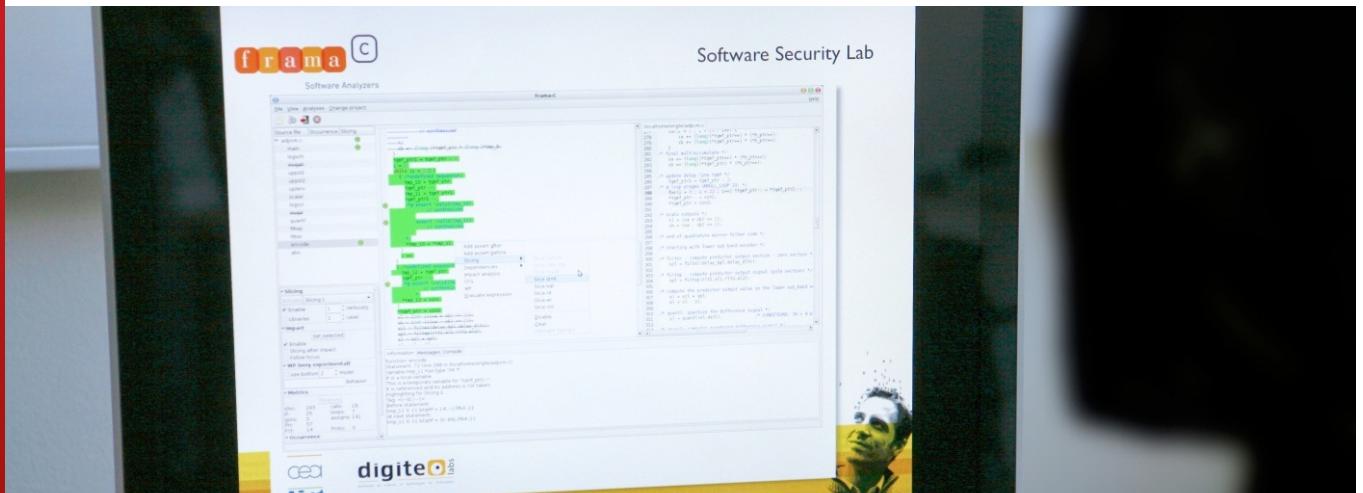


list

www.cea.fr

REGION MODEL

> frama-c -wp-model region



QLCC Septembre 2018 | Loïc Correnson

REGION ANALYSIS : FRAMA-C / WP-REGION

Recap :

- ✓ need for **smarter** memory models
- ✓ there exists typical separation **patterns**
- ✓ users shall be able to **specify** separation
- ✓ common patterns shall be **inferred**

Implemented in 2017 :

- ✓ region and pattern annotations
- ✓ memory points-to & data layout analysis
- ✓ inférence of never-alias patterns
- ✓ memory model selection is **almost** there

REGION ANALYSIS : FRAMA-C / WP-REGION

Recap :

- ✓ need for **smarter** memory models
- ✓ there exists typical separation **patterns**
- ✓ users shall be able to **specify** separation
- ✓ common patterns shall be **inferred**

2018 Objectives

- ✓ compound-copies (tracked, but not used)
- ✓ points-from analysis
- ✓ pessimistic vs. optimistic aliasing patterns
- ✓ memory model for WP

REGION ANALYSIS : FRAMA-C / WP-REGION

Recap :

- ✓ need for **smarter** memory models
- ✓ there exists typical separation **patterns**
- ✓ users shall be able to **specify** separation
- ✓ common patterns shall be **inferred**

Implemented in 2018

- ✓ compound-copies (tracked, but not used)
- ✓ points-from analysis
- ✓ ~~pessimistic vs. optimistic aliasing patterns~~
- ✓ memory model for WP

IMPLEMENTATION

REGION ANNOTATIONS

ACSL Extensions

New Clause for Behaviors

```
@region region-spec, ... ;
```

Region Specifications: represent a collections of memory locations (aka « regions »)

```
region-spec ::=  
| ident: region-spec      // The region is given a name  
| \pattern{ident}, ...     // The region shall follow the pattern(s)  
| l-path, ...             // The region contains the given locations
```

Locations: represent sets of l-values (aka « l-paths »)

```
l-path ::=  
| ident                  // Named memory region  
| ident                  // C-variables (in scope)  
| ( c-type ) l-path      // C-Casts  
| * l-path                // Pointer access  
| l-path [ range ]        // Array or Pointer access  
| l-path +( range )       // Pointer shift  
| l-path . ident          // Field access  
| l-path -> ident         // Field arrow  
| ( l-path.ident? .. l-path.ident? ) // Field range  
| ( l-path->ident? .. l-path->ident? ) // Field arrow range
```

```
<range> ::= <exp> | <exp>?..<exp>?
```

REGION ANALYSIS : FRAMA-C / WP-REGION

Extension of Frama-C / WP

Frama-C / WP : New Options

-region-annot	Register '@region' ACSL Annotations (auto with -wp-region) (opposite option is -no-region-annot)
-wp-region	Perform Region Analysis (experimental) (opposite option is -wp-no-region)
-wp-region-cluster	Compute region clustering fixpoint (set by default, opposite option is -wp-no-region-cluster)
-wp-region-fixpoint	Compute region aliasing fixpoint (set by default, opposite option is -wp-no-region-fixpoint)
-wp-region-flat	Flatten arrays by default (opposite option is -wp-no-region-flat)
-wp-region-inline	Inline aliased sub-clusters (set by default, opposite option is -wp-no-region-inline)
-wp-region-pack	Pack clusters by default (set by default, opposite option is -wp-no-region-pack)
-wp-region-rw	Written region are considered read-write by default (set by default, opposite option is -wp-no-region-rw)

REGION ANALYSIS : FRAMA-C / WP-REGION

Analyses Complémentaires de Region

Frama-C / WP : Visualisation des Analyses

-wp-msg-key dot, pdf	Output memory graphs with names <f>.{dot, pdf}
-wp-msg-key offset	Dump l-path access in memory graphs
-wp-msg-key deref	Dump typed access to each region
-wp-msg-key cluster	Dump data-layout analysis in memory graphs
-wp-msg-key from	Dump from analysis
-wp-msg-key roots	Dump roots analysis
-wp-msg-key chunk	Dump memory chunk allocation
-wp-msg-key garbled	Warns on garbled-mix regions

REGION ANALYSIS : FRAMA-C / WP-REGION

Infrastructure de Tests pour les régions

Fichiers de tests unitaire (x19)

```
$ cd src/plugins/wp/tests/wp_region
$ ls *.i
annot.i          array4.i          array8.i          index.i          structarray3.i
array1.i          array5.i          fb_ADD.i         matrix.i          structarray4.i
array2.i          array6.i          fb_SORT.i        structarray1.i swap.i
array3.i          array7.i          garbled.i        structarray2.i
```

Script utilisateur (en plus de Frama-C ./bin/pTests.opt)

```
$ cd src/plugins/wp/tests/wp_region
$ ./fc.sh -h
fc.sh [options...] <test.[ic]>
-h,--help      help and exit
-D,--delete   clean output directory and exit
-g,--gui       run in Frama-C Gui
-r,--region    visualize region graph
-u,--update    commit region graph in oracle
-t,--test      run ptests.opt on test file (or all files)
-q,--qualif   run ptests.opt with test-config qualif
--open <cmd>  opens pdf with '<cmd>'
-k <keys>     set message keys
*             any other Frama-C options
```

NOUVELLES ANALYSES

REGION ANALYSIS : FRAMA-C / WP-REGION

Analyses Matricielles (test struct-array #1)

```
$ cd src/plugins/wp/tests/wp_region  
$ ./fb.sh -r structarray1.i
```

```
typedef struct Vector {  
    int coord[4];  
} * vector ;
```

```
typedef struct Matrix {  
    int coef[4][4];  
} * matrix ;
```

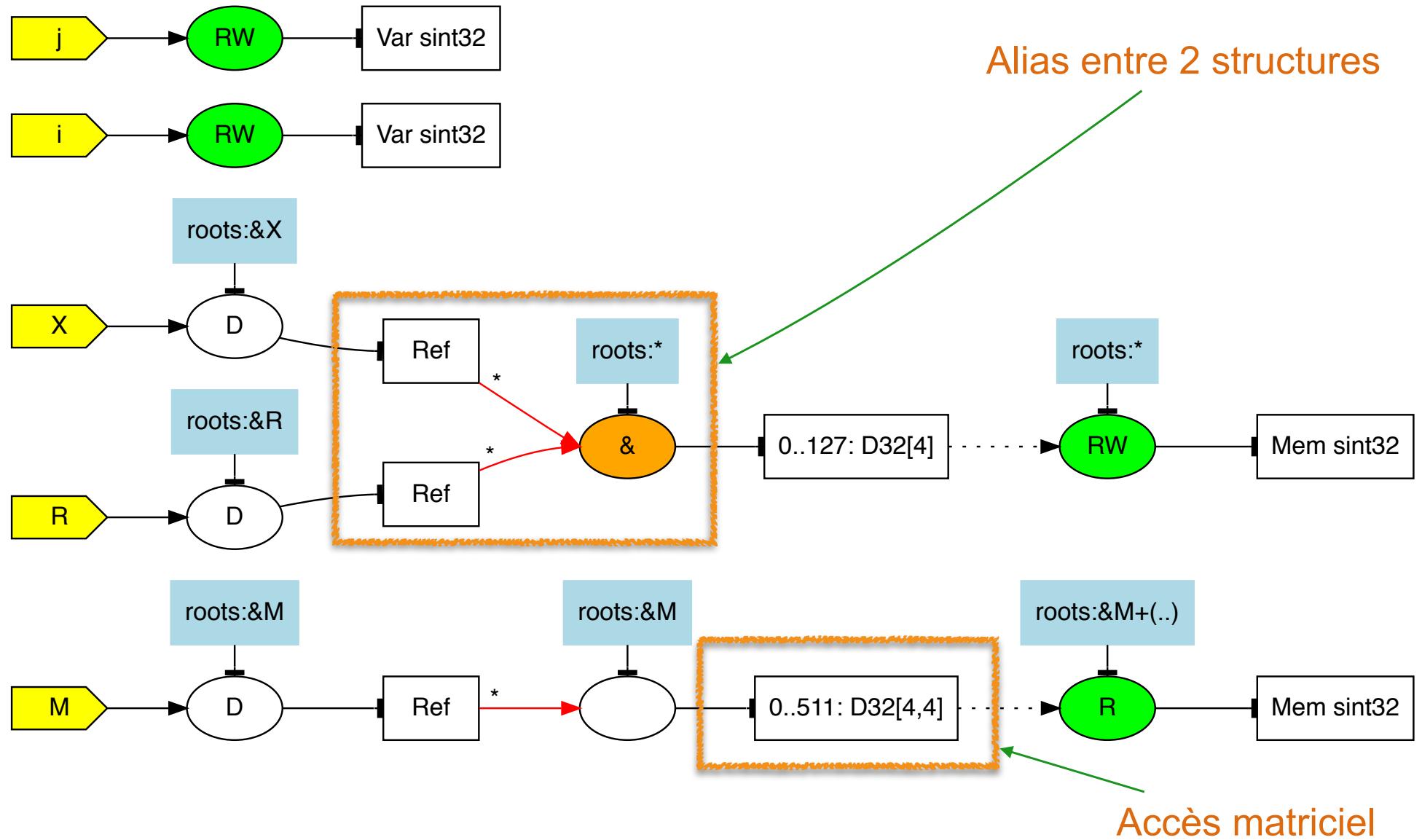
```
//@ region *X , *R ;
```

```
void job( matrix M , vector X , vector R )  
{  
    for (int i = 0; i < 4; i++) {  
        R->coord[i] = 0 ;  
        for (int j = 0; j < 4; j++) {  
            R->coord[i] += M->coef[i][j] * X->coord[j];  
        }  
    }  
}
```

Alias entre 2 structures

Accès matriciel

REGION ANALYSIS : FRAMA-C / WP-REGION



REGION ANALYSIS : FRAMA-C / WP-REGION

Analyses Matricielles (test struct-array #2)

```
$ cd src/plugins/wp/tests/wp_region
$ ./fb.sh -r structarray2.i

typedef struct Vector {
    int coord[4];
} * vector ;

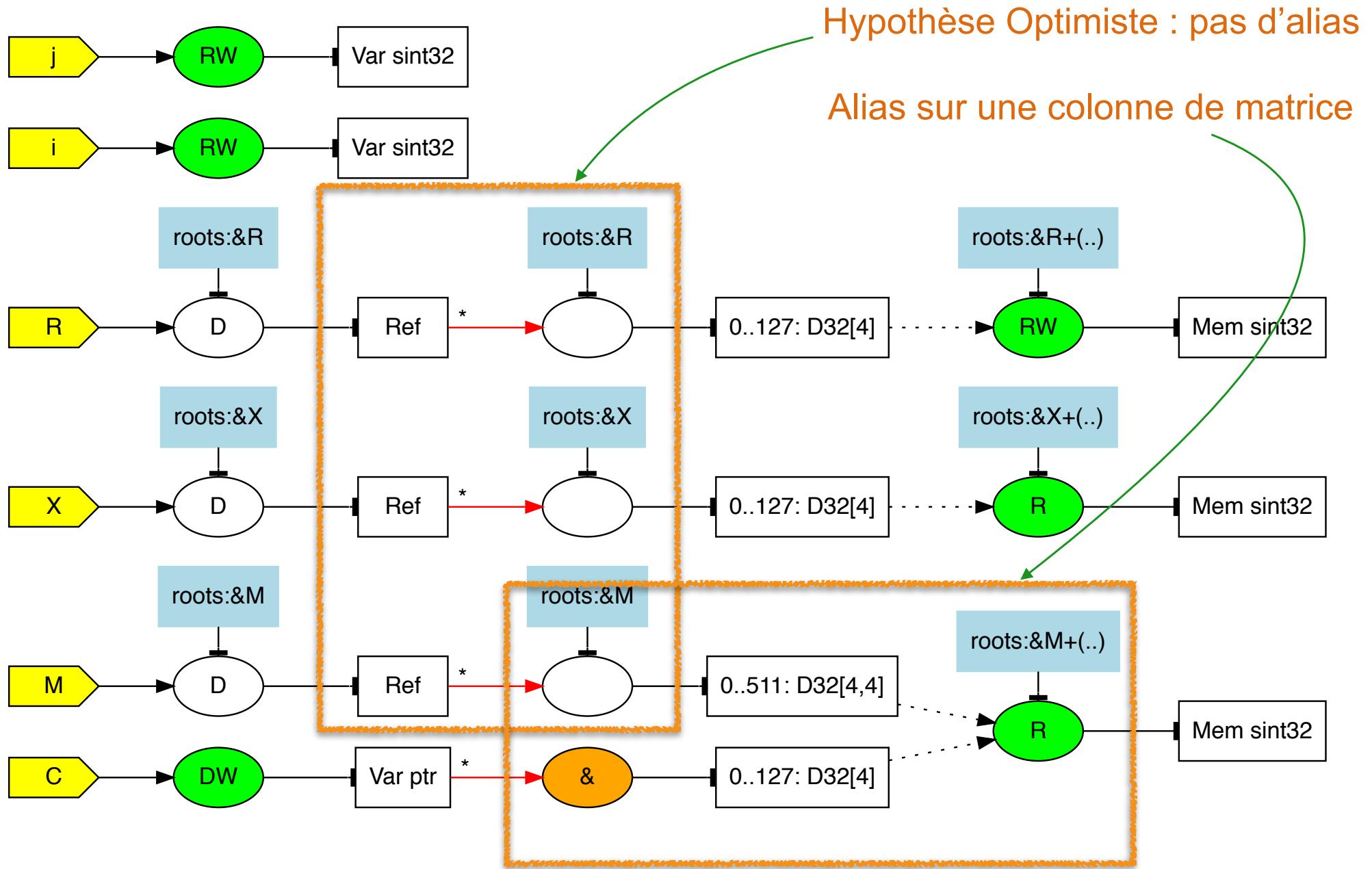
typedef struct Matrix {
    int coef[4][4];
} * matrix ;

void job( matrix M , vector X , vector R )
{
    for (int i = 0; i < 4; i++) {
        R->coord[i] = 0 ;
        for (int j = 0; j < 4; j++) {
            vector C = (vector) (M->coef[i]) ;
            R->coord[i] += C->coord[j] * X->coord[j];
        }
    }
}
```

Hypothèse Optimiste : pas d'alias

Alias sur une colonne de matrice

REGION ANALYSIS : FRAMA-C / WP-REGION



REGION ANALYSIS : FRAMA-C / WP-REGION

Analyses de Root (test fb_ADD)

```
$ cd src/plugins/wp/tests/wp_region
$ ./fb.sh -r fb_ADD.i

typedef struct N { double v ; int s ; } *SN ;
typedef struct L { int v ; int s ; } *SL ;

typedef struct Block {
    SN prm ;
    SN inp1 ;
    SN inp2 ;
    SN inp3 ;
    SN out1 ;
    SN out2 ;
    SN out3 ;
    SL idx1 ;
    SL idx2 ;
    SL idx3 ;
    SN sum ;
} FB ;

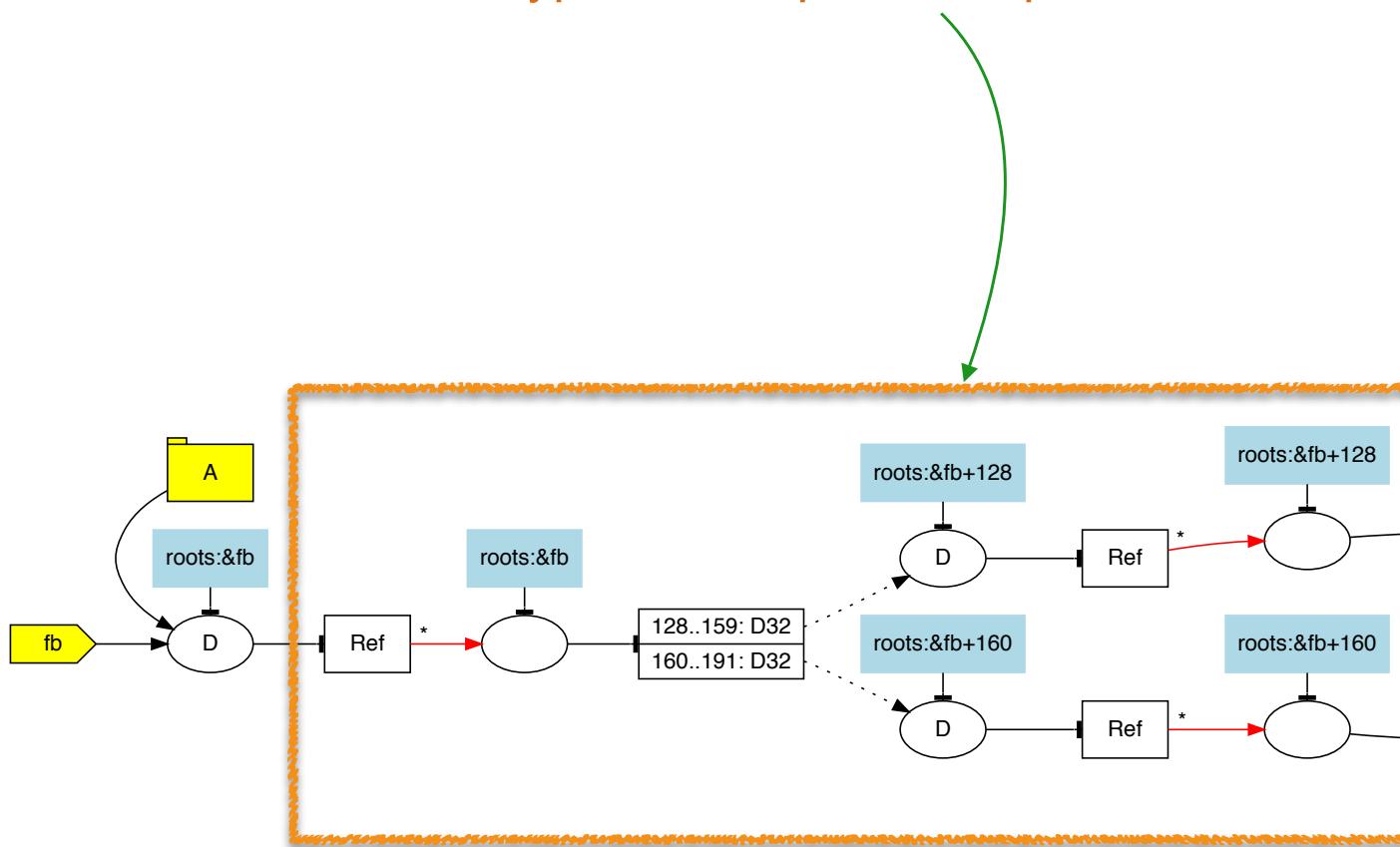
/*@
  region A: fb ;
*/
void job(FB *fb)
{
    fb->out1->v = fb->out1->v + fb->out2->v ;
    fb->out1->s = fb->out1->s | fb->out2->s ;
}
```

Hypothèse Optimiste : pas d'alias

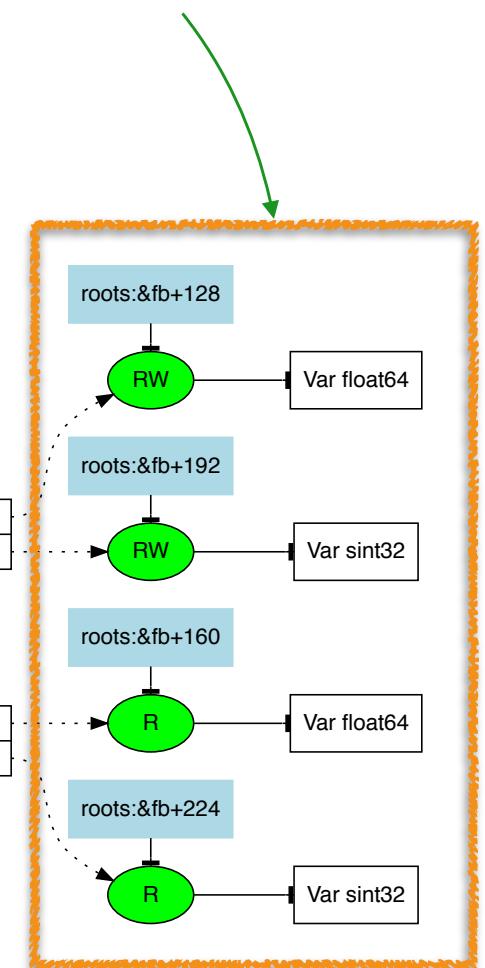
Pas d'accès indexé

REGION ANALYSIS : FRAMA-C / WP-REGION

Hypothèse Optimiste : pas d'alias



Pas d'accès indexé



REGION ANALYSIS : FRAMA-C / WP-REGION

Analyses de Root (test fb_SORT)

```
$ cd src/plugins/wp/tests/wp_region
$ ./fb.sh -r fb_SORT.i
```

```
/*@
region Shared: *(fb->inp1 .. fb->inp3);
region IN:      (fb->inp1 .. fb->inp3);
region OUT:     (fb->out1 .. fb->out3);
region IDX:     (fb->idx1 .. fb->idx3);
*/
void job(FB *fb)
```

```
{  
    SN *inp = &(fb->inp1) ;  
    SN *out = &(fb->out1) ;  
    SL *idx = &(fb->idx1) ;
```

```
for (int i = 0; i < 3; i++) {  
    out[i]->v = inp[i]->v + fb->prm->v ;  
    out[i]->s = 0 ;  
    idx[i]->v = inp[i]->s ;  
    idx[i]->s = 0 ;  
}
```

```
fb->sum->v =  
    fb->out1->v +  
    fb->out2->v +  
    fb->out3->v ;
```

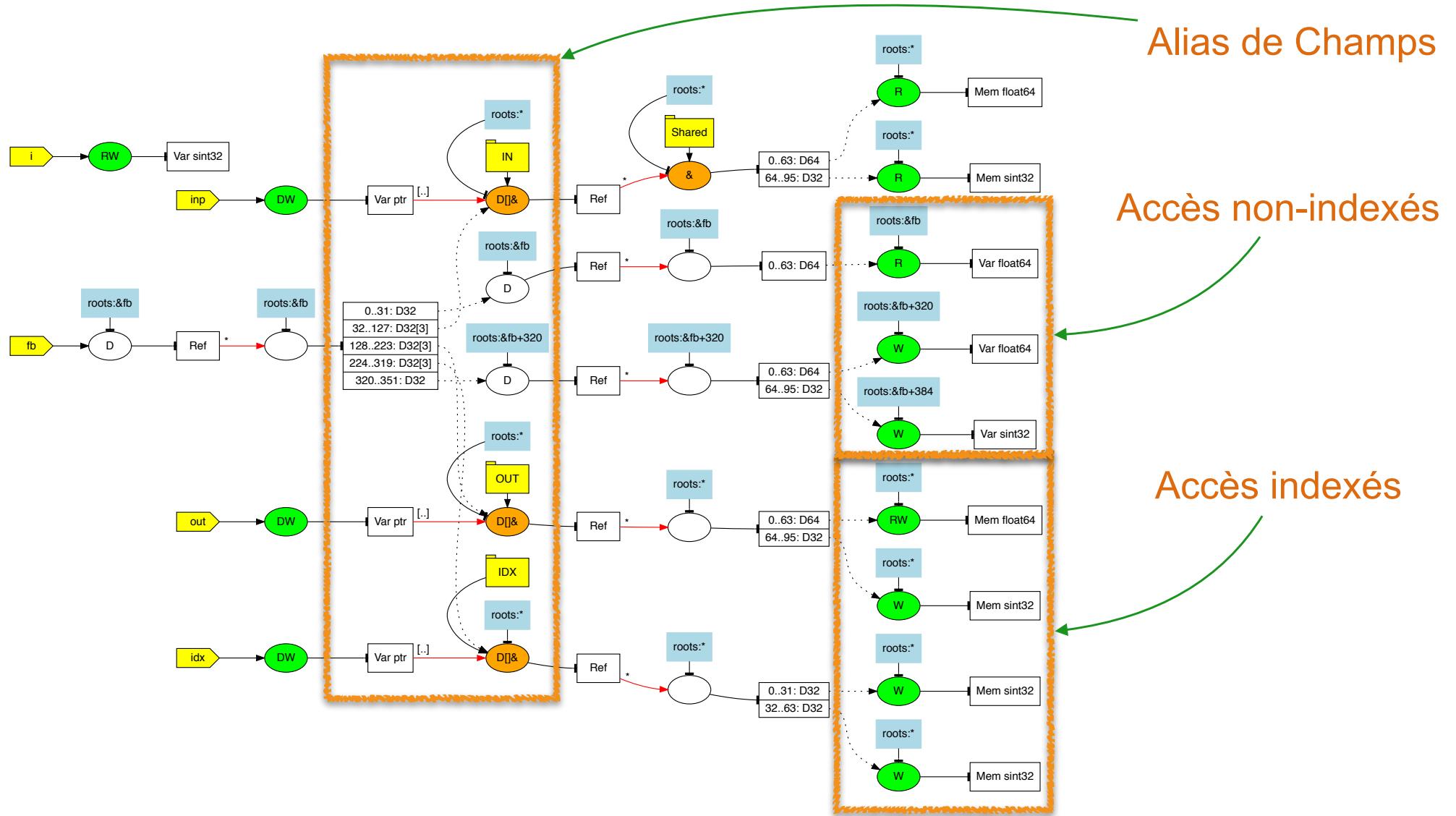
```
fb->sum->s = 0 ;
```

Alias de Champs

Accès indexés

Accès non-indexés

REGION ANALYSIS : FRAMA-C / WP-REGION



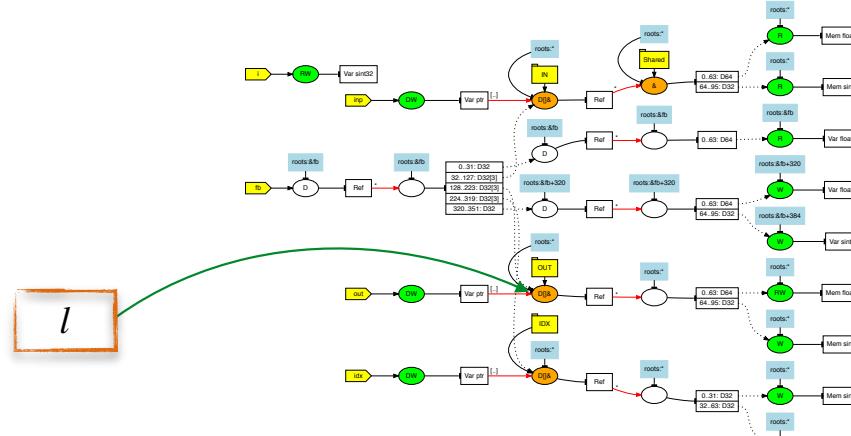
MODELE MEMOIRE

REGION MODEL

Modèle CompCert (de référence)

Adresse : $A ::= \{ \text{addr} : \mathbb{Z} ; \text{offset} : \mathbb{Z} \}$

Mémoire : $\begin{aligned} \text{read_sint32} &: (M, A) \rightarrow \mathbb{Z} \\ \text{write_sint32} &: (M, A, \mathbb{Z}) \rightarrow M' \end{aligned}$



Modèle Region

Index : $l \in L \quad r \in R$ (position dans le graphe des régions)

Concrétisation : $\text{addrof} : L \rightarrow A$ (adresse d'un index, non-injectif)

Cohérence : $\text{consistent} : L \rightarrow \text{Prop}$ (respect des plages d'index autorisées)

Mémoire (non-indexée) : $x_r : \mathbb{Z}$ (région (r) représentant *un* unique entier)
 $x_r : L$ (région (r) représentant *un* unique pointeur)

Mémoire (indexée) : $m_r : [A \mapsto \mathbb{Z}]$ (région (r) représentant *des* entiers)
 $m_r : [A \mapsto L]$ (région (r) représentant *des* pointeurs)

(pour les pointeurs,
on mémorise **l'index**
et non l'adresse
pour conserver
le chemin d'origine
dans le graphe)

REGION MODEL (CORRECTION)

Modèle CompCert (de référence)

Adresse : $A ::= \{ \text{addr} : \mathbb{Z} ; \text{offset} : \mathbb{Z} \}$

Mémoire : $\text{read_sint32} : (M, A) \rightarrow \mathbb{Z}$
 $\text{read_pointer} : (M, A) \rightarrow A$

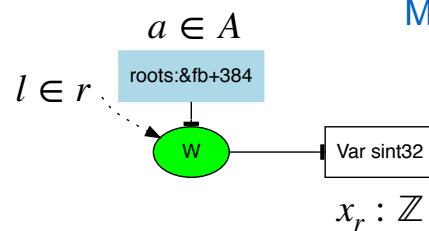
Modèle Region

Index : $l \in L \quad r \in R$

Concrétisation : $\text{addrof} : L \rightarrow A$

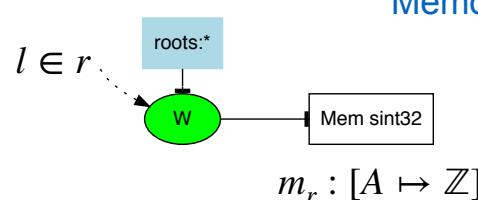
Cohérence : $\text{consistent} : L \rightarrow \text{Prop}$

Propriétés de Cohérence :



Mémoire (entier non-indexé) :

$$\forall l \in r, \text{consistent}(l) \implies \text{addrof}(l) = a \wedge x_r = \text{read_sint32}(M, a)$$



Mémoire (entiers indexés) :

$$\forall l \in r, \text{consistent}(l) \implies m_r[\text{addrof}(l)] = \text{read_sint32}(M, \text{addrof}(l))$$

REGION MODEL (CORRECTION)

Modèle CompCert (de référence)

Adresse : $A ::= \{ \text{addr} : \mathbb{Z} ; \text{offset} : \mathbb{Z} \}$

Mémoire : $\text{read_sint32} : (M, A) \rightarrow \mathbb{Z}$
 $\text{read_pointer} : (M, A) \rightarrow A$

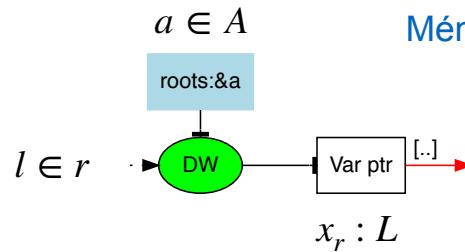
Modèle Region

Index : $l \in L \quad r \in R$

Concrétisation : $\text{addrof} : L \rightarrow A$

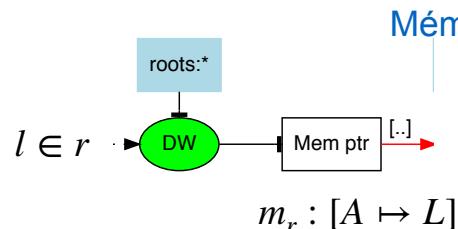
Cohérence : $\text{consistent} : L \rightarrow \text{Prop}$

Propriétés de Cohérence :



Mémoire (pointeur non-indexé) :

$$\forall l \in r, \text{consistent}(l) \implies \text{addrof}(l) = a \wedge \text{addrof}(x_r) = \text{read_pointer}(M, a)$$



Mémoire (pointeurs indexés) :

$$\forall l \in r, \text{consistent}(l) \implies \text{addrof}(m_r[\text{addrof}(l)]) = \text{read_pointer}(M, \text{addrof}(l))$$

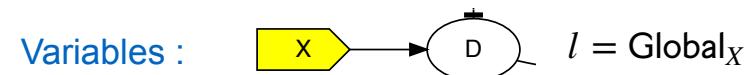
REGION MODEL (INDEX)

Index : $l \in L \quad r \in R$

Concrétisation : $\text{addrf} : L \rightarrow A$

Cohérence : $\text{consistent} : L \rightarrow \text{Prop}$

Modèles & Cohérence des Index



$\text{addrf}(\text{Global}_X) = \{\text{base} = \text{Base}_X; \text{offset} = 0\}$

$\text{consistent}(\text{Global}_X) \iff \text{true}$

REGION MODEL (INDEX)

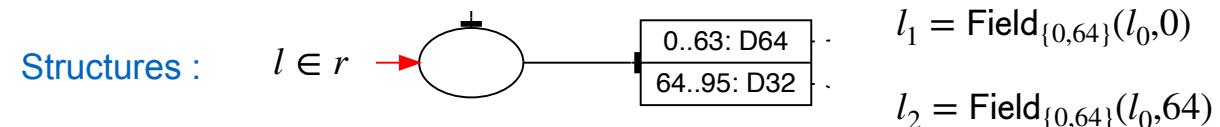
Index : $l \in L \quad r \in R$

Concrétisation : $\text{addr} : L \rightarrow A$

Cohérence : $\text{consistent} : L \rightarrow \text{Prop}$

$$\{ \text{base} = a ; \text{offset} = i \} \oplus k = \{ \text{base} = a ; \text{offset} = i + k \}$$

Modèles & Cohérence des Index



$$\begin{aligned} \text{addr}(\text{Field}_S(l, k)) &= \text{addr}(l) \oplus k \\ \text{consistent}(\text{Field}_S(l, k)) &\iff \text{consistent}(l) \wedge k \in S \end{aligned}$$

REGION MODEL (INDEX)

Index : $l \in L \quad r \in R$

Concrétisation : $\text{addrf} : L \rightarrow A$

Cohérence : $\text{consistent} : L \rightarrow \text{Prop}$

$$\{ \text{base} = a ; \text{offset} = i \} \oplus k = \{ \text{base} = a ; \text{offset} = i + k \}$$

Modèles & Cohérence des Index

Tableaux : $l \in r \xrightarrow{\quad} \boxed{0..511: \text{D32}[4,4]} \cdot l' = \text{Array}_{32,[4,4]}(l, i, j)$

$$\text{addrf}(\text{Array}_{s,d_s}(l, k_1 \dots k_n)) = \text{addrf}(l) \oplus s \cdot \sum_i (k_i \cdot \prod_{j < i} d_j)$$

$$\text{consistent}(\text{Array}_{s,d_s}(l, k_1 \dots k_n)) \iff \text{consistent}(l) \wedge \forall i, 0 \leq k_i < d_i$$

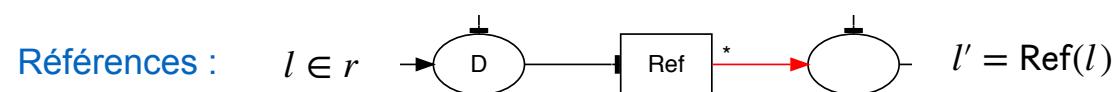
REGION MODEL (INDEX)

Index : $l \in L \quad r \in R$

Concrétisation : $\text{addr} : L \rightarrow A$

Cohérence : $\text{consistent} : L \rightarrow \text{Prop}$

Modèles & Cohérence des Index



$$\text{addr}(\text{Ref}(l)) = \varphi(\text{addr}(l)) \quad \varphi : A \rightarrow A \text{ (injective)}$$

$$\text{consistent}(\text{Ref}(l)) \iff \text{consistent}(l)$$

CONCLUSION

REGION ANALYSIS : FRAMA-C / WP-REGION

Implemented in 2018

- ✓ compound-copies (tracked, but not used)
- ✓ points-from analysis
- ✓ pessimistic vs. optimistic aliasing patterns
- ✓ memory model for WP
- ✓ consistency checks generated, but not yet verified
- ★ unsupported ACSL functions & predicates

2019 Objectives

- ✓ compound-copies (tracked, but not used)
- ✓ memory patterns
- ✓ consistency checks verification
- ✓ full ACSL support

THANKS !